
High Performance Embedded Architectures And Compilers First International Conference Hipeac 2005 B

virtual graphs for high-performance embedded topologies - virtual graphs for high-performance embedded topologies whitepaper summary many optimization and machine learning algorithms are commonly described as graph problems. **design of high-performance embedded system** - performance estimation based on duty cycle specification [9]. using hipere, the designs are evaluated and compared manually to identify the final design. 2.1 using milan for device selection for design of high-performance low-power embedded systems initially, using milan we model the target application as a hierarchical data flow graph (figure 1). **rapidio: the interconnect architecture for high ...** - future high performance embedded systems. in embedded system applications, rapidio has just a limited to no impact on the software infrastructure that operates over it. it can be implemented with relatively few transistors and it offers low operation latency and high bandwidths. high performance embedded equipment often **tie language—the fast path to high-performance embedded ...** - tie language—the fast path to high-performance embedded soc processing while using custom rtl accelerator blocks in data-intensive functions can provide high performance, they may require long design and verification cycle times. as an alternative, processor-based designs **high-performance embedded workshop v.4.09 user's manual** - high-performance embedded workshop v.4.09 introduction r20ut0372ej0100 rev.1.00 feb 01, 2011 about this user's manual this user's manual describes the high-performance embedded workshop system. this user's manual describes information on the basic "look and feel" of the high-performance embedded workshop and customizing the high- **high performance embedded computing (hpec) workshop** - 2002 high performance embedded computing workshop theme the focus of this workshop is on high performance embedded computing technologies. the hpec workshop will give u.s. government funded researchers from academia, industry, and government, working in this important area, an opportunity to discuss **high performance embedded er16ef gpu** - high performance embedded er16ef gpu delivering high performance graphics or compute for embedded applications. built in the pci express form factor with a x16 pci express 3.0 interface, it delivers a long life gpu solution for a variety of embedded applications that utilize standard form factor motherboards and chassis. **high-performance embedded workshop v.4.07 user's manual** - high-performance embedded workshop introduction rej10j2037-0100 rev.1.00 sep. 01, 2009 about this user's manual this user's manual describes the high-performance embedded workshop system. this user's manual describes information on the basic "look and feel" of the high-performance embedded workshop and customizing the high- **high performance embedded computing - abaco** - high performance embedded computing . parallelism has been at the heart of supercomputing since the term was first used in the 1960s. today, supercomputing has, in many instances, become high performance computing - but instead of vast rooms full of mainframe computers all working together on the same problem, high

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